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Patent
Attorney's Docket No. 033116-002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

J1040 U.S. PTO
09/886081
06/18/01



In re Patent Application of)
Gorin et al.) Group Art Unit:
Application No.:)
Filed: June 18, 2001)
For: CDM SIMULATOR FOR TESTING)
ELECTRICAL DEVICES)

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants
hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98.
Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

U.S. Patent 5,740,007, Nakaie et al., issued April 14, 1998; and
"Field-Induced Charged-Device Model Test Method for Electrostatic Discharge Withstand
Thresholds of Microelectronic Components", JEDEC STANDARD, JESD22-C101, Electronic
Industries Association, Engineering Department, May 1995

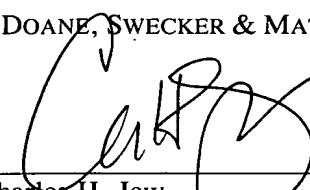
The documents are being submitted within 3 months of the filing or entry of the national
stage of this application or before the first Office Action on the merits, whichever is later, therefore
no fee or certification is required under 37 C.F.R. § 1.97(b).

Information Disclosure Statement
Application No.
Attorney's Docket No. 033116-002
Page 2

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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